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He is a coauthor of more than 60 scientific research papers and two other textbooks: Fundamentals of Digital Logic with Verilog Design, 2nd ed. and Field-Programmable Gate Arrays. He has won multiple awards for excellence in teaching electrical engineering,...

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He is a coauthor of two other books: Fundamentals of Digital Logic with VHDL Design and Field-Programmable Gate Arrays. Zvonko Vranesic received his B.A.Sc., M.A.Sc., and Ph.D. degrees, all in Electrical Engineering, from the University of Toronto.

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