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The optimization of these devices for VLSI applications is also covered. The authors highlight the intricate interdependencies and subtle trade-offs between those device parameters, such as power consumption and packing density, that affect circuit performance and manufacturability.

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Target Audience: Graduate students and advanced undergraduates in electrical engineering, computer engineering and physics interested in VLSI devices and circuits. Course Description: Study of device phenomena in very small and high-speed devices including effects of scaling, interfaces, and high doping. Control of electrical characteristics (threshold voltage, breakdown voltage, on/off currents, current gain, switching speed) in small MOS structures.

Physics and Modeling of Nanoscale VLSI Devices

FUNDAMENTALS OF MODERN VLSI DEVICES YUAN TAUR TAK H. IXIIXIG CAMBRIDGE UNIVERSITY PRESS . CONTENTS Physical Constants and Unit Conversions page xi List of Symbols xlii Preface xxi Ⅸ INTRODUCTION 1 1.1 Evolution of VLSI Device Technology 1 1.2 Modern VLSI Devices 4

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